

### **JMP (1100)** Instruction Cycle

|      |    |    |    |     |    |   |    |   |   |        |   |   |   |   |   |
|------|----|----|----|-----|----|---|----|---|---|--------|---|---|---|---|---|
| 15   | 14 | 13 | 12 | 11  | 10 | 9 | 8  | 7 | 6 | 5      | 4 | 3 | 2 | 1 | 0 |
| 1100 |    |    |    | 000 |    |   | BR |   |   | 000000 |   |   |   |   |   |

#### Instruction Fetch

**MAR**  $\leftarrow$  **PC**

**PC**  $\leftarrow$  **PC** + 1

**MDR**  $\leftarrow$  **memory**[**MAR**]

**IR**  $\leftarrow$  **MDR**

#### Decode

**IR**[15:12] **decoded**

#### Evaluate Address

n/a

#### Operand Fetch

n/a

#### Execute

**PC**  $\leftarrow$  **R**<sub>**IR**[8:6]</sub> + 0

#### Store Result

n/a